**Developing Soft and Parallel Programming Skills Using Project-Based Learning**

Spring-2019 **JupitorInk**

Nhi Ong, Alejandro Herbener, Alec Buchinski, Johnathan Moore

**Table of Contents**

1. Project Description……………………………………………………………………2
   1. Purpose of the Project…………………………………………………………….2
2. Introduction to the Team………………………………………………………………2

2.1 Members Roles and Assignments…………………...……………………………2

1. Planning and Scheduling……………………………………………………………3-4

3.1 Role as Coordinator……………………………………………………………….3

3.2 Additional Tasks……...………………………………………………………...3-4

1. Parallel Programming Skills……………………………………………………….4-11

5.1 Parallel Programming Report Task 3a……..……………………………………4-6

5.2 Parallel Programming Report Task 3b….......………..……………………..…6-11

1. ARM Assembly Programming…………………………...………………...….…12-16
2. Appendix…………………………………………………………………………......17
3. **Project Description**
   1. **Purpose of the Project**

The purpose of this project is to 1) become familiar with unified, team-based project management tools and communication applications in order to develop soft skills, 2) identify Raspberry Pi 3+ components and architecture; explain basic multicore architectures and programming using OpenMP and C language 3) gain more practice with ARM assembly programming (Mussa, Assignment 2 Worksheet).

1. **Introduction to the Team**
   1. **Members’ Roles and Assignments**

Coordinator: Alec Buchinski

For this assignment, he was the coordinator; his responsibilities contained tasks one, two and six. Task one involves group planning and scheduling and creating a table for task breakdowns. A new GitHub repository was created for assignment 3, sent out invites to the slack and access to upload to the GitHub as well. The final duties involved editing and uploading the video for part 6 to the YouTube channel.

Team Member: Alejandro Herbener

This team member is responsible for task five. I created this report after the other members completed their sections and compiled them into this document.

Team member: Nhi Ong

This team member is responsible for task three. Nhi completed the Parallel Programming portion of this project, as well as taking screenshots and reporting their findings.

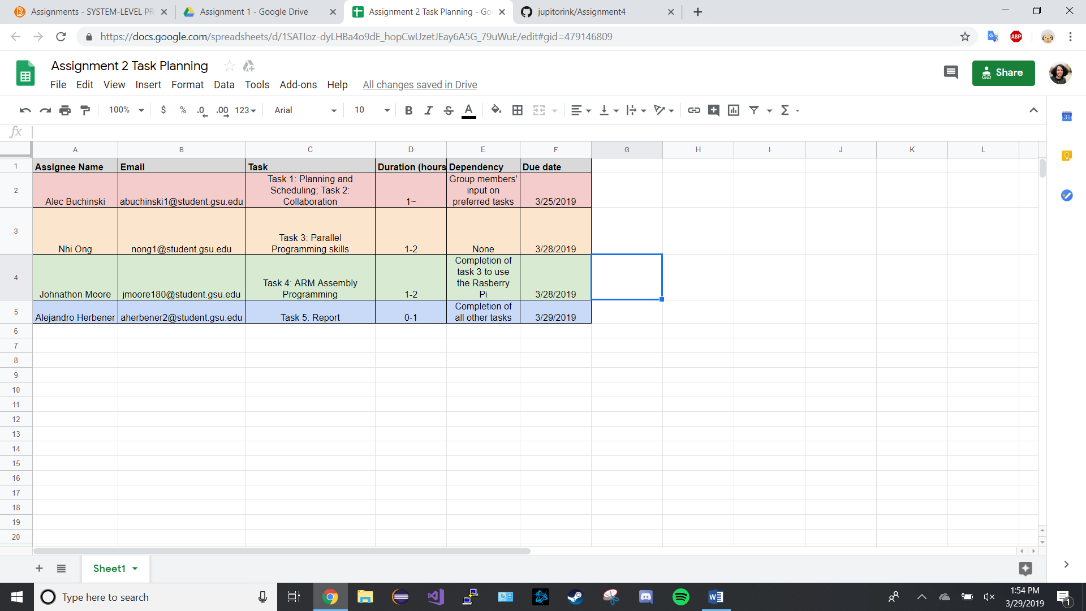
Team Member: Johnathon Moore

This team member is responsible for task four. Johnathon completed the ARM Assembly Programming section of this project. This includes taking screenshots, completing the assigned work, and creating a write-up to send to the GitHub for further editing.

1. **Planning and Scheduling**

**3.1 Role as Coordinator**

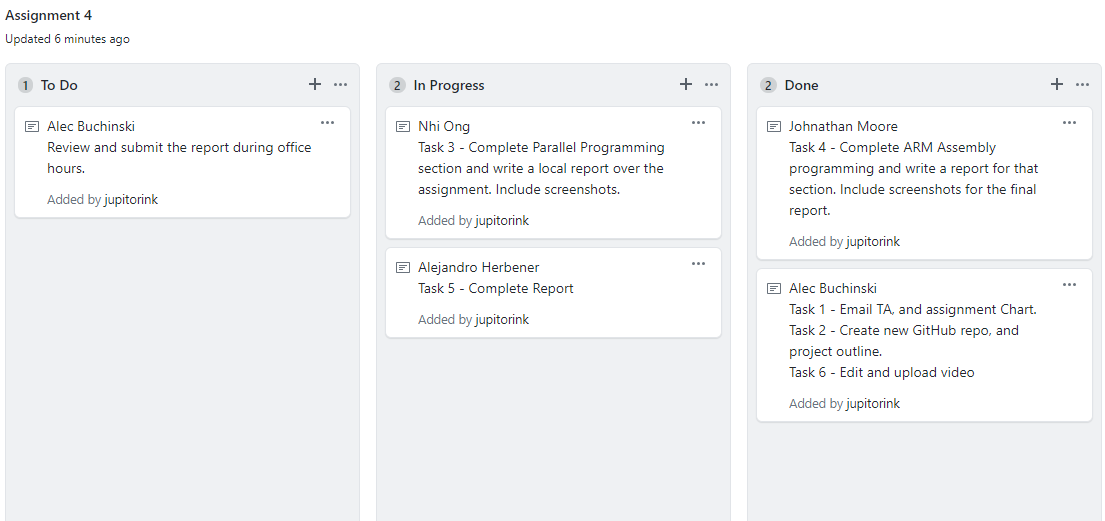
As the coordinator, Alec completed the new assignment table and created a new repository for the assignment. He contacted us to discuss our preferred roles over Slack, and we selected our preferred roles and quickly started to work on each of our sections as seen in figure 1.1.



*Figure 1.1 – Task One, Assignment Chart*

**3.2 Additional Tasks**

Alec was responsible for creating a new repository for this assignment with a readme page. He added a projects page which details our tasks and allows for any member to update their status on their role. (see Figure 1.2) In addition to this, he also edited our video and uploaded it to YouTube. (see Appendix A, for link to [video](https://www.youtube.com/watch?v=eWah_4RSt8M&feature=youtu.be))



*Figure 1.2 – Task Two, GitHub Projects Page Screenshot*

1. **Parallel Programming Skills**

**Task 3a.**

***What is a race condition?***

A race condition occurs within software or systems when there is a dependency on the sequence of other events beyond the programmer’s control that affects the output.

***Why race condition is difficult to reproduce and debug?***

Race conditions are hard to reproduce because often, the ending results or output will vary despite using the same input and conditions, since it depends on the timing of the threads.

***How can it be fixed? Provide an example from your project\_A3.***

You can fix this issue by having good software architecture to prevent the issue from occurring in the first place. An example where the issue is fixed beforehand comes from Assignment 3’s Parallel Programming task. The parallel for pragma without the reduction clause resulted in an incorrect outcome, but the solution to this problem simply required declaring the variables as private so that each thread had its own clone. This fixed the issue because the sum variable was dependent on each of the threads’ processes during computation.

***Summarize the Parallel Programming Patterns section in the “Introduction to Parallel Computing\_3.pdf” in your own words (one paragraph, no more than 150 words)***

Parallel programs have patterns which are used often by programmers due to their success in previous programs. These patterns are documented for new programmers to use. There are two major categories for the patterns that developers use: strategies and concurrent execution mechanism. Strategies simply refer to the programmer’s choice on which “algorithmic strategies” to use as well has how to implement those strategies. Concurrent execution mechanisms have two major categories as well: process/thread control patterns and coordination patterns. Coordination patterns can further be separated into message passing and mutual exclusion. Process/thread control patterns involve the processing units and how they are executed at runtime whereas coordination patterns involve determining how processing units coordinate to complete tasks.

***Compare collective synchronization (barrier) with Collective communication (reduction) and Master-worker with fork join****.*

Barriers are used to enforce collective synchronization by adding wait cycles into a program where a process cannot continue until the other processes have reached the same point, where they will be synchronized. Reductions are used to enforce collective communication by combining multiple vectors of processes or processes into one. Both patterns are concurrent execution mechanisms.

A Master-Worker pattern uses a master process/thread to hold a group of worker processes with a certain number of tasks. The “workers” complete each task simultaneously while removing them from the tasks held by the master process. The Fork-Join pattern describes the process of forking parent tasks to create new tasks and completing them concurrently with other forked tasks, then waiting until they finish executing before joining them further continue the parent task. Both patterns are strategies.

***Where can we find parallelism in programming?***

You can find them in loops!

***What is dependency and what are its types?***

A dependency occurs when the execution of one operation is reliant on the outcome of a previous operation and its execution. There are two types of dependencies—independent and dependent.

Independent:

Statement 1 – a = 3;

Statement 2 – b = 3;

Dependent:

Statement 1 – a = 3;

Statement 2 – b = a;

***When a statement is dependent and when it is independent?***

A dependency is independent if changing the order of execution does not affect the outcome. Otherwise, if changing the order causes an error or results in an undesirable outcome, then the dependency is dependent.

Independent:

Statement 1 – a = 33;

Statement 2 – b = 33;

Dependent:

Statement 1 – a = b;

Statement 2 – b = 33;

***When can two statements be executed in parallel?***

Two statements can be executed in parallel when the order of the operations do not affect the outcome and the statements themselves, do not have any dependencies.

***How can dependency be removed?***

Dependencies can be removed by changing the order of the statements or removing some of them completely.

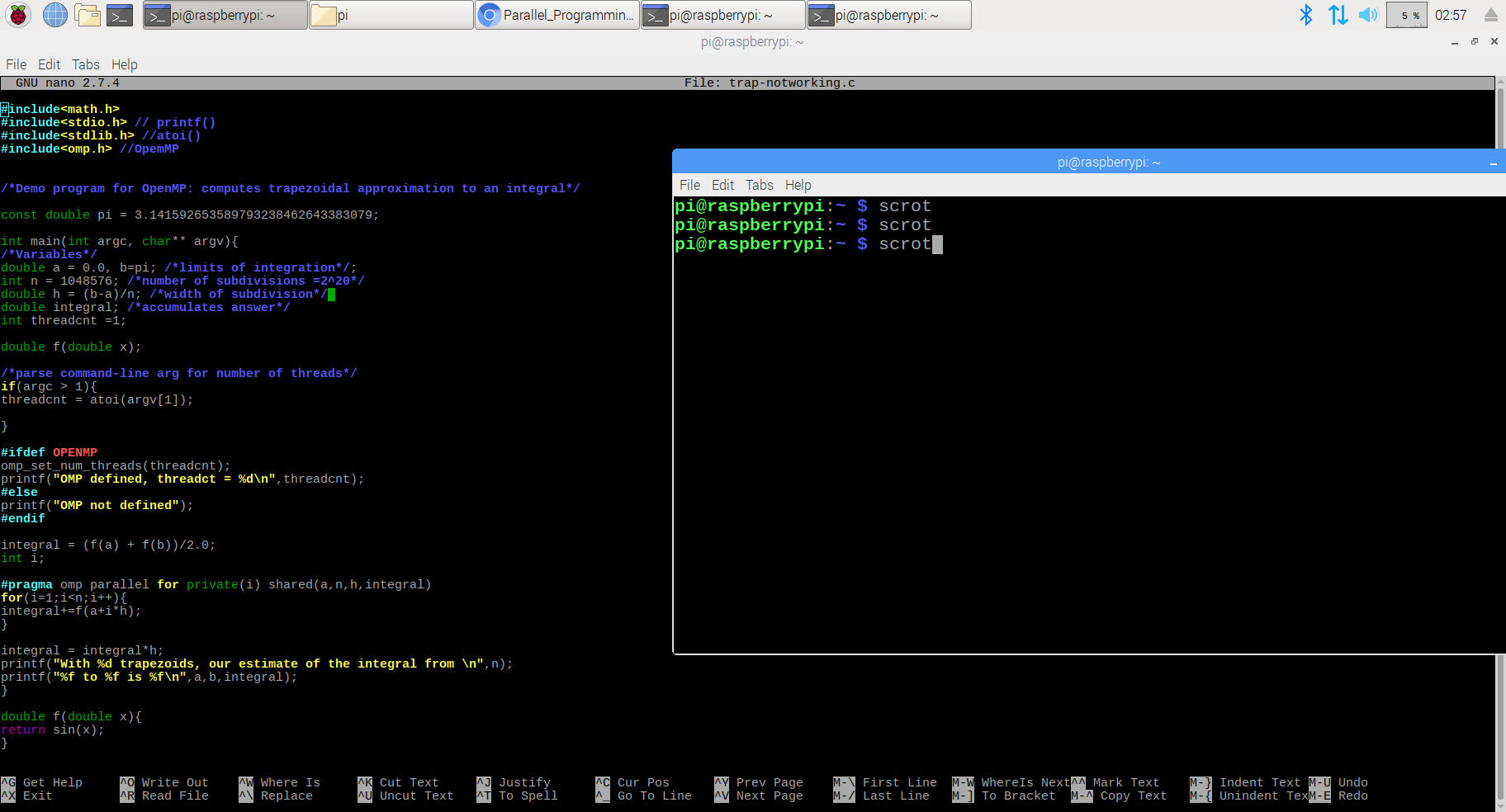
***How do we compute dependency for the following two loops and what type/s of dependency?***

The two loops are dependent on the value of i in order to find the correct index of a and set the correct value to it.

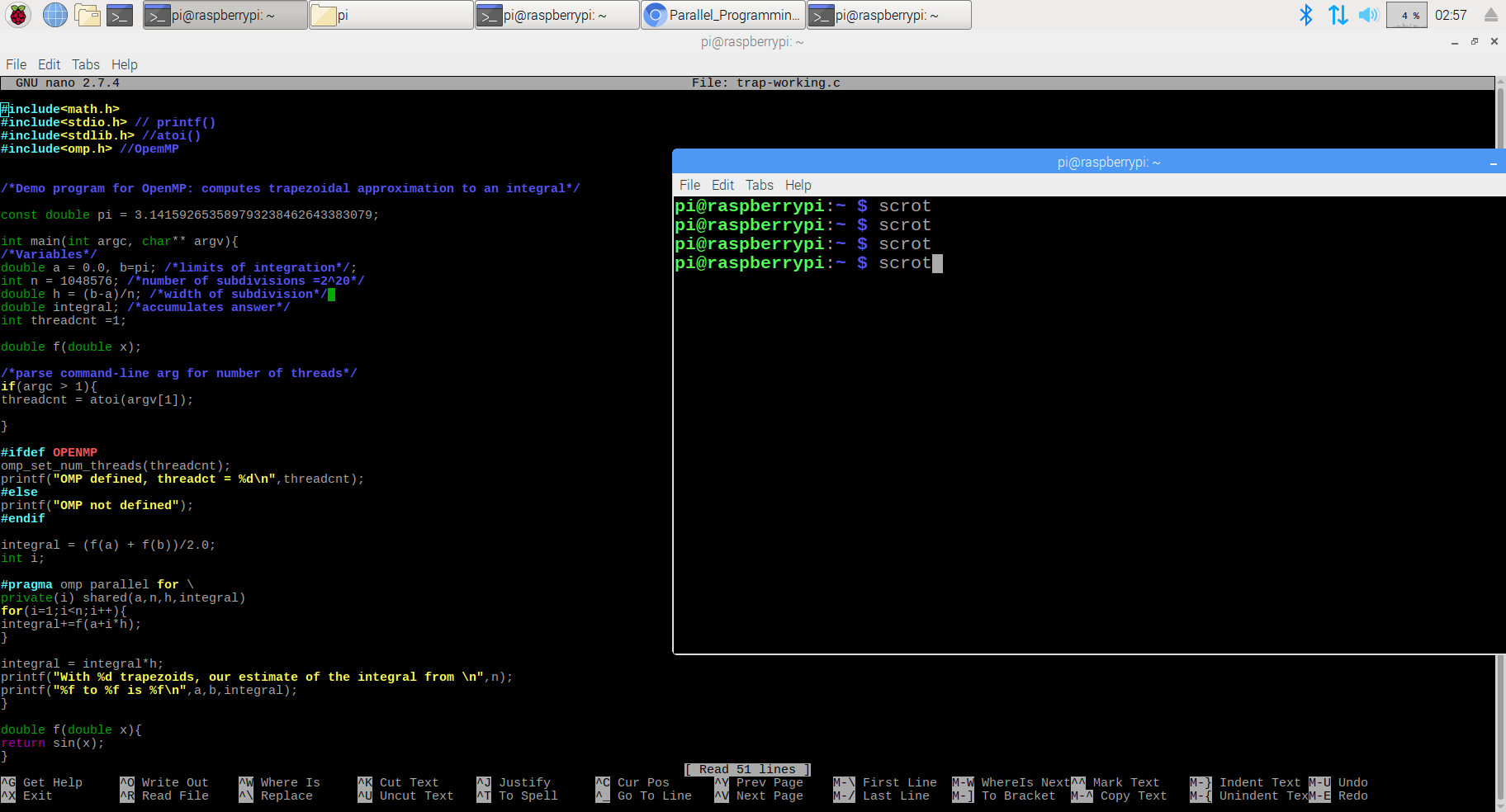
**5.2 Task 3b.**

**Part One: Integration Using the Trapezoidal Rule**

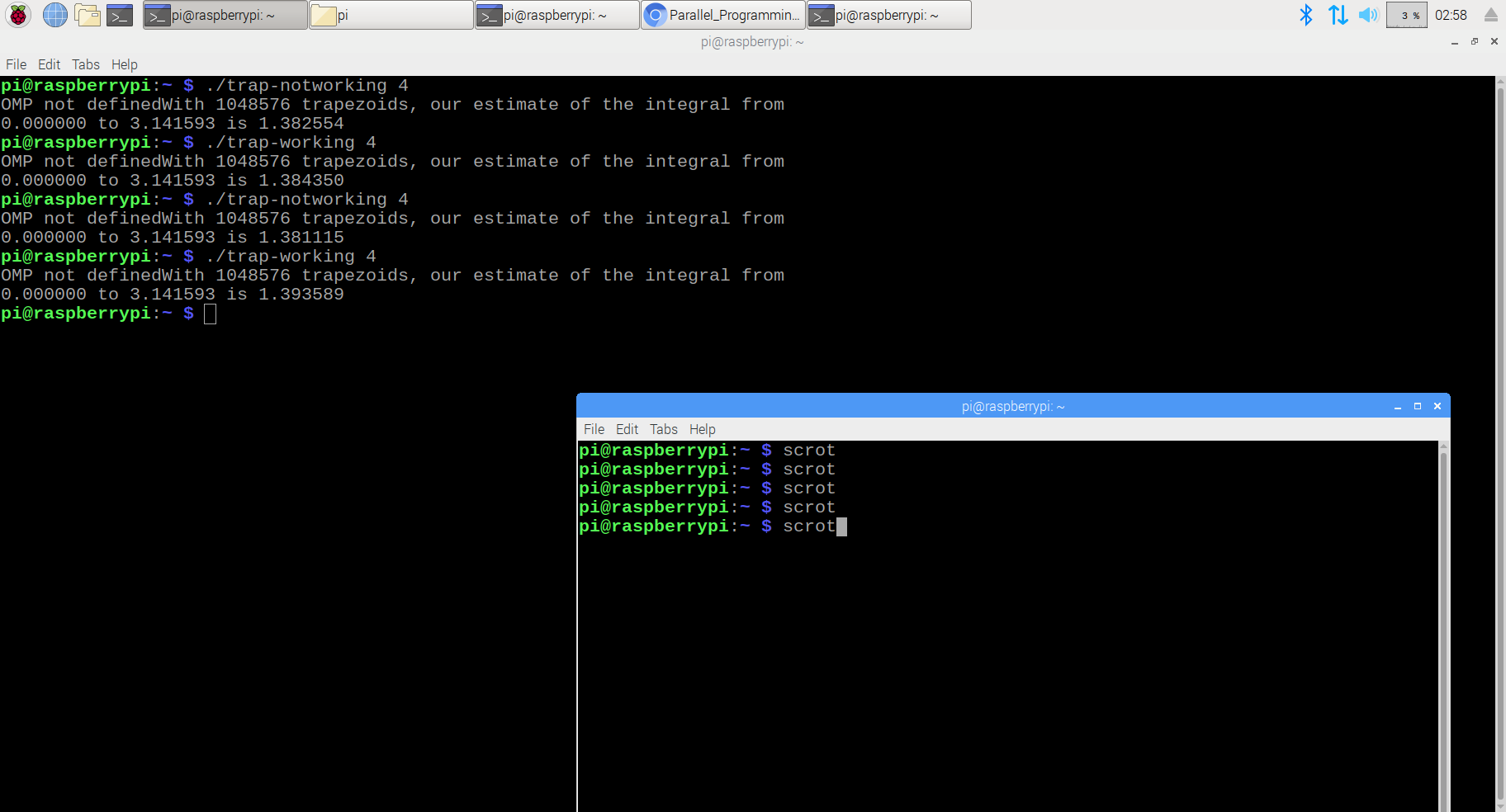
For the first part of this section, I created a file called trap-notworking.c using nano and copied he code provided on the pdf file. I then created an executable as instructed.



After that, I created a new file called trap-working.c and corrected the error by adding a backslash after “#pragma omp parallel for” and moving the line with “private” to the next line. I then created another executable.

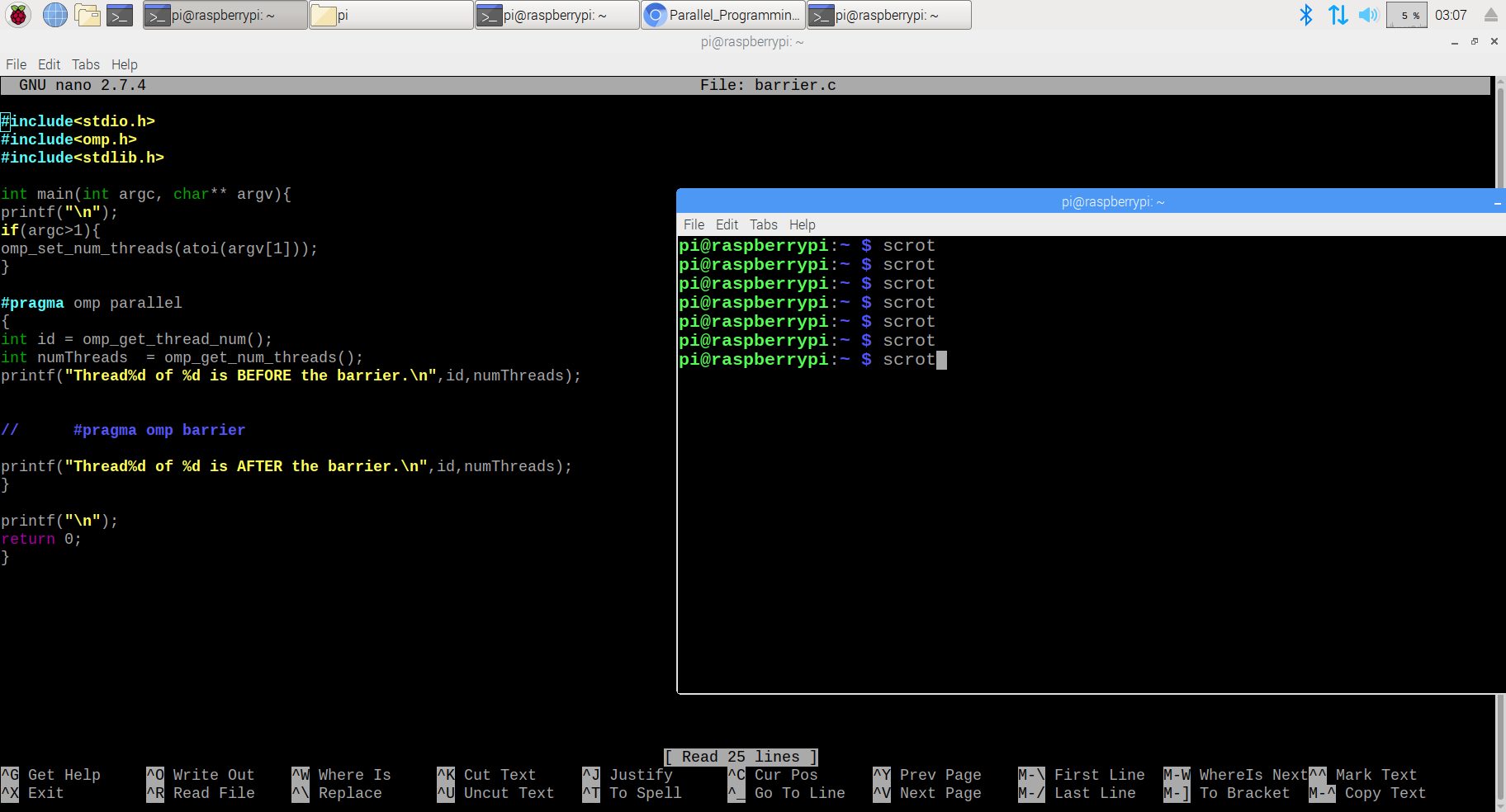


I compared the results between two of the programs multiple times to check if the intended result was reached.

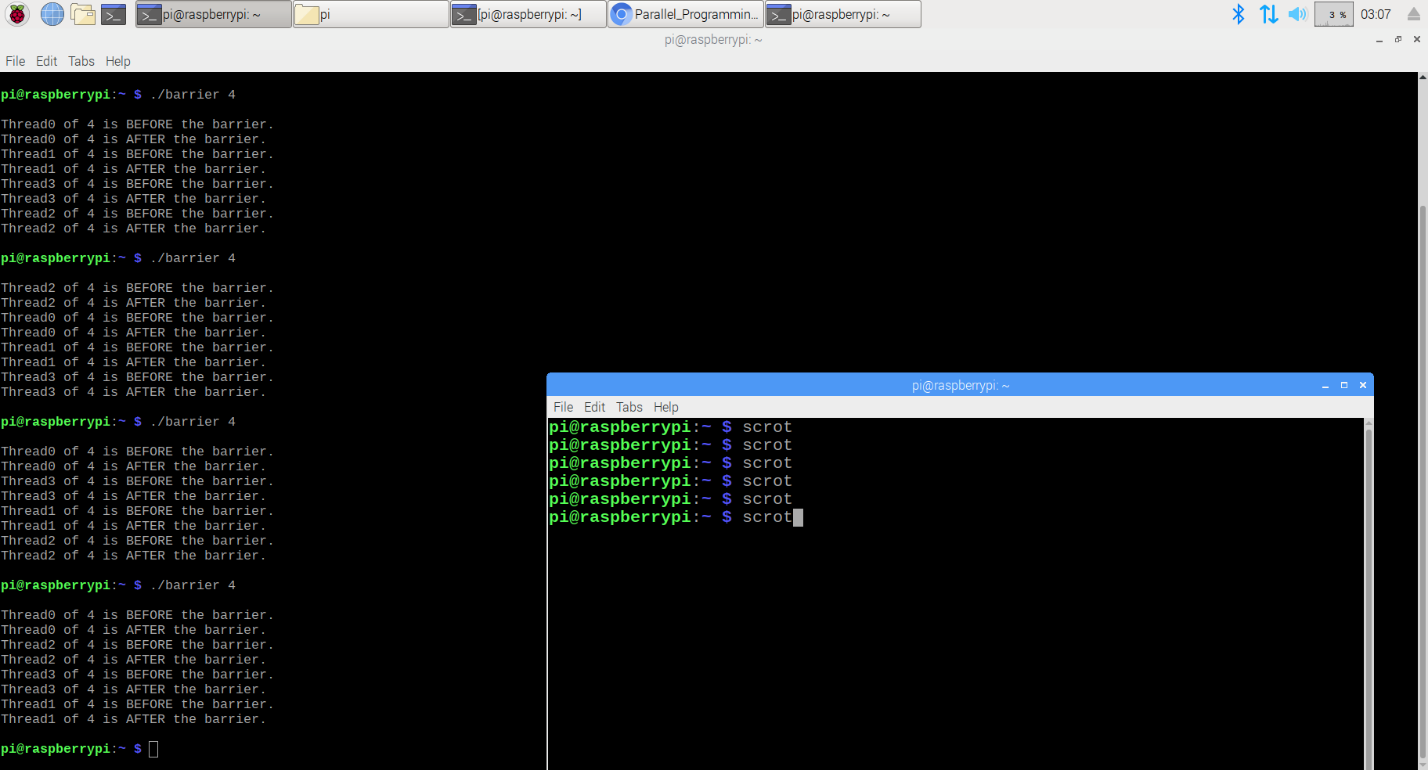


**Part Two: Coordination: Synchronization with a Barrier**

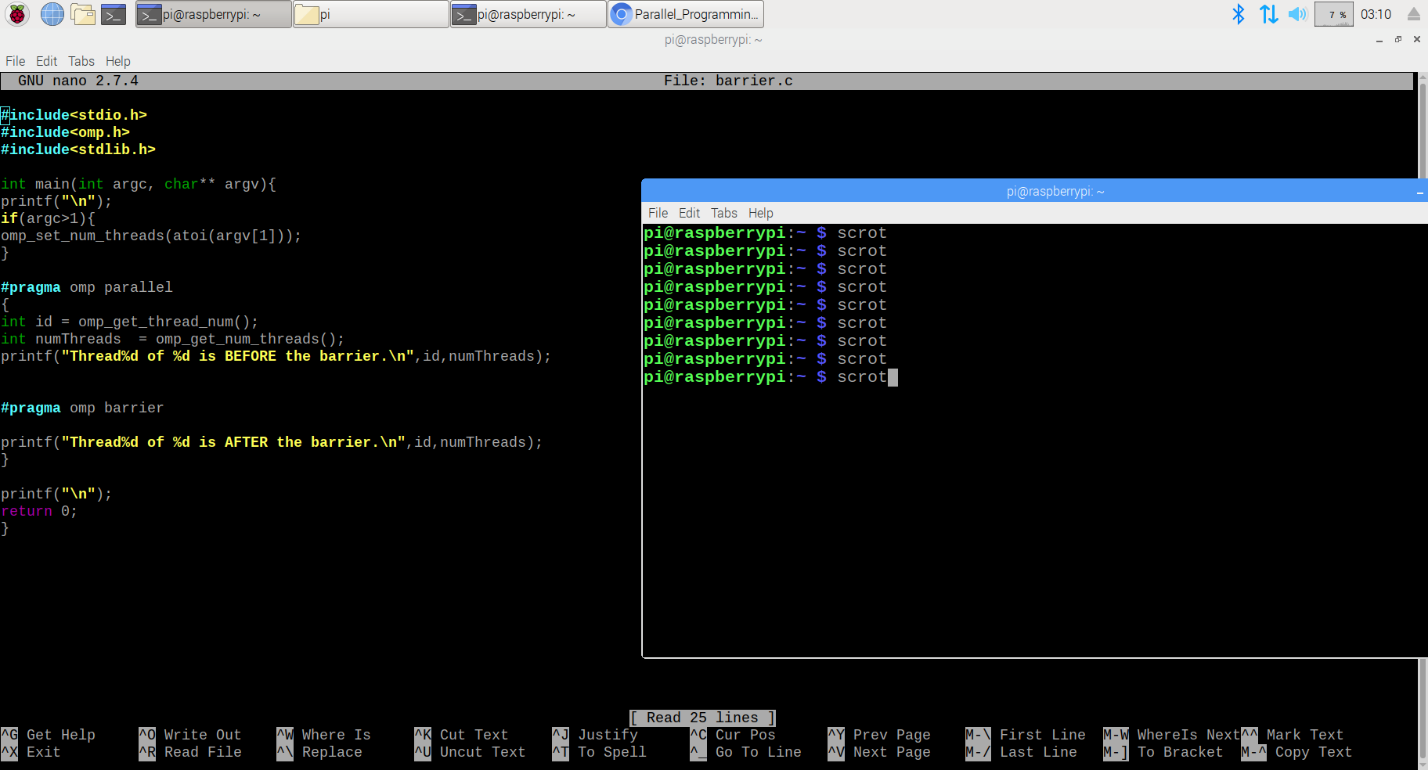
For this section of the task, I read the short excerpt on barriers and then proceeded to complete the programming portion. I created a barrier.c file and copied the code from the pdf.



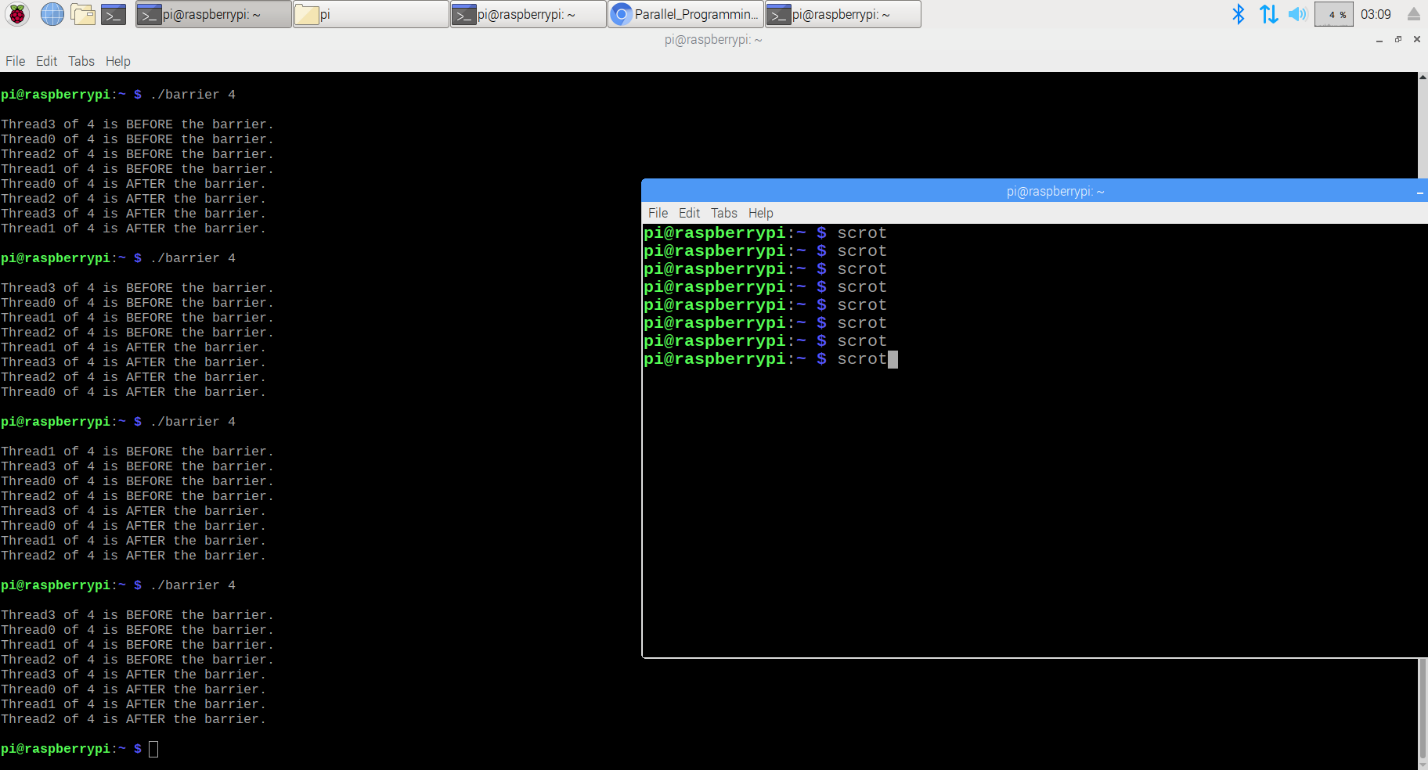
I created an executable for this program and ran it with the “#pragma omp barrier” line still commented out.



As you can see, with the barrier not effect, the program simply runs the operations in random, but sequential order, one after another without stopping. Continuing the task, I uncommented the “#pragma omp barrier” line and ran the program once again.

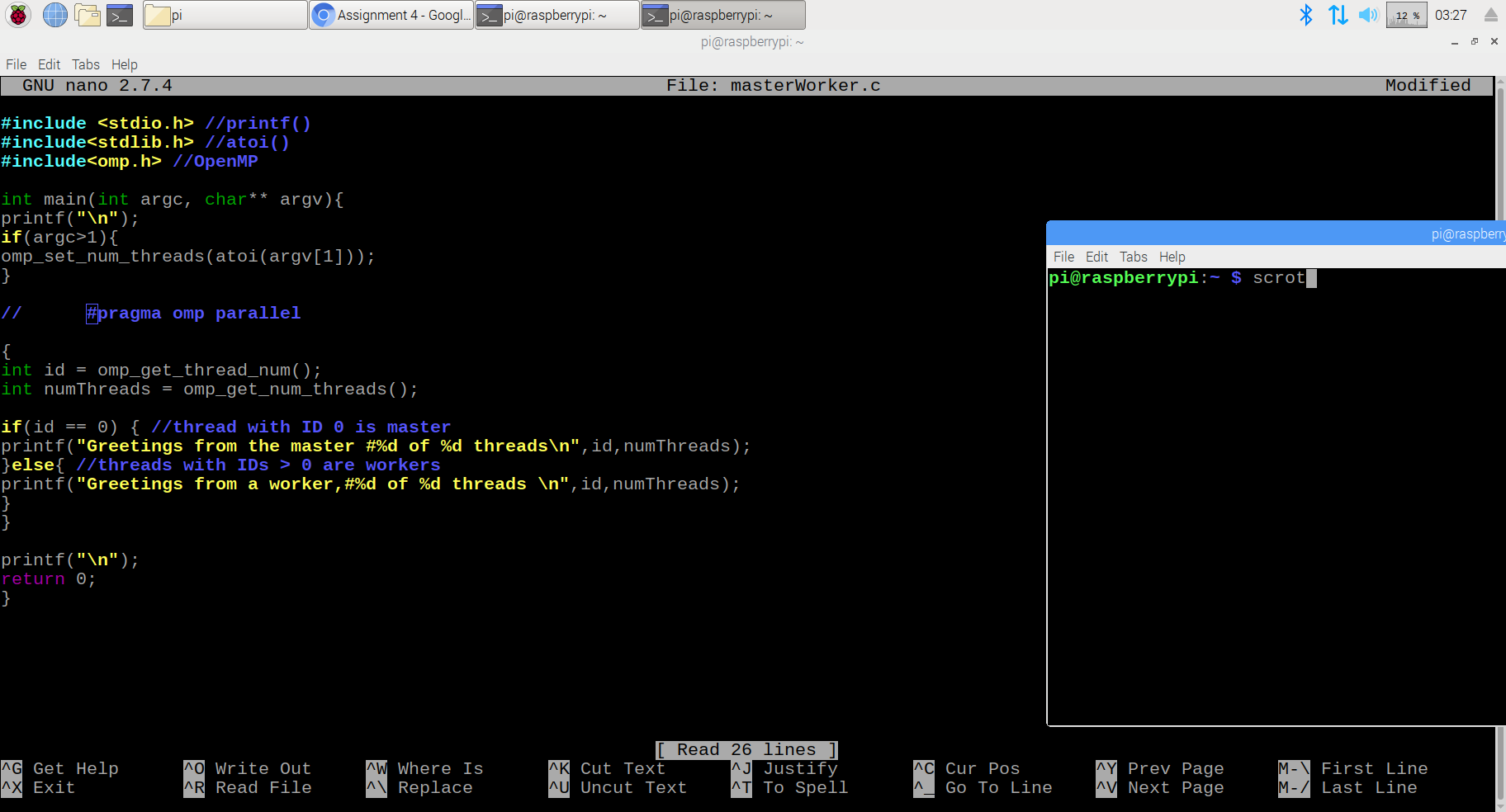


With the barrier in effect this time, you can see that the threads stop before the barrier and continue executing together after the barrier, in contrast to what we observed earlier without the barrier.

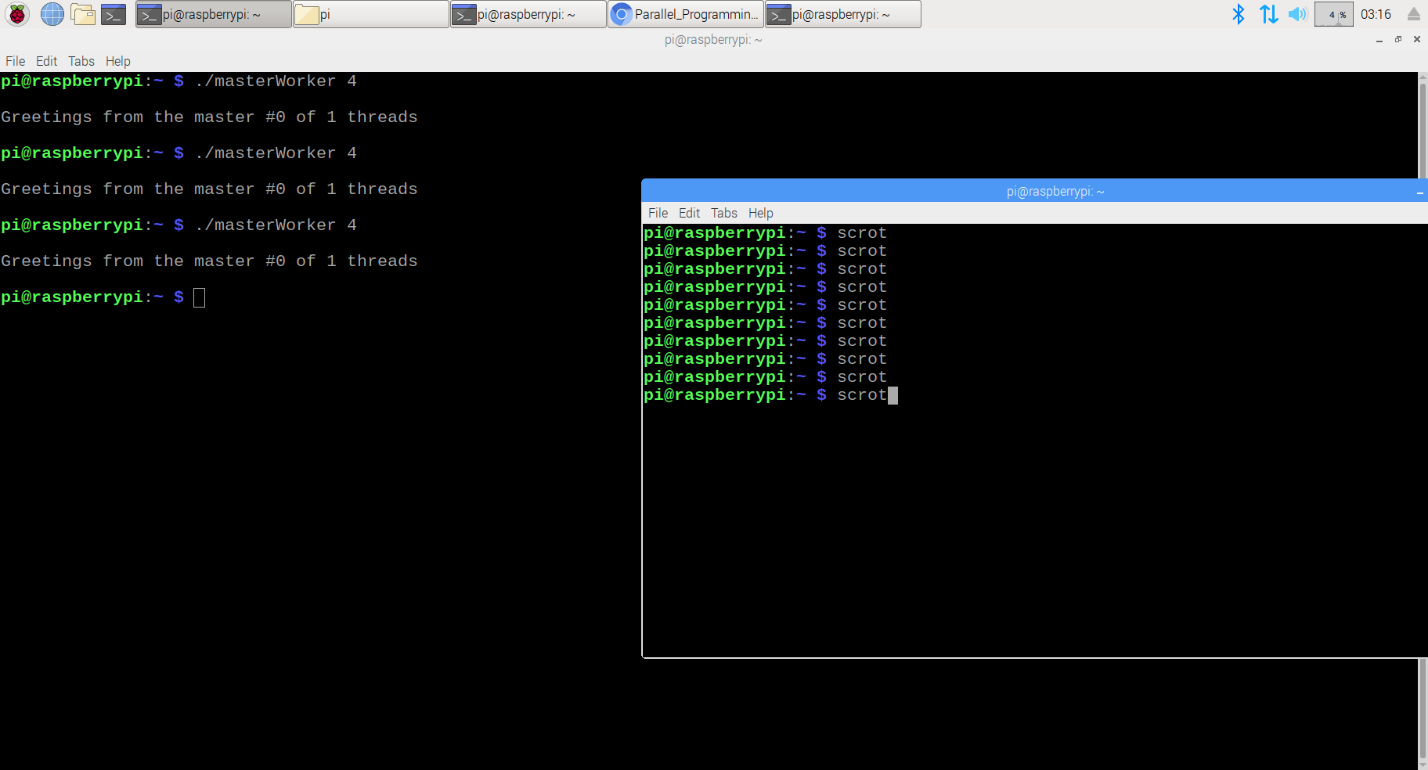


**Part Three: Program Structure: The Master-Worker Implementation Strategy**

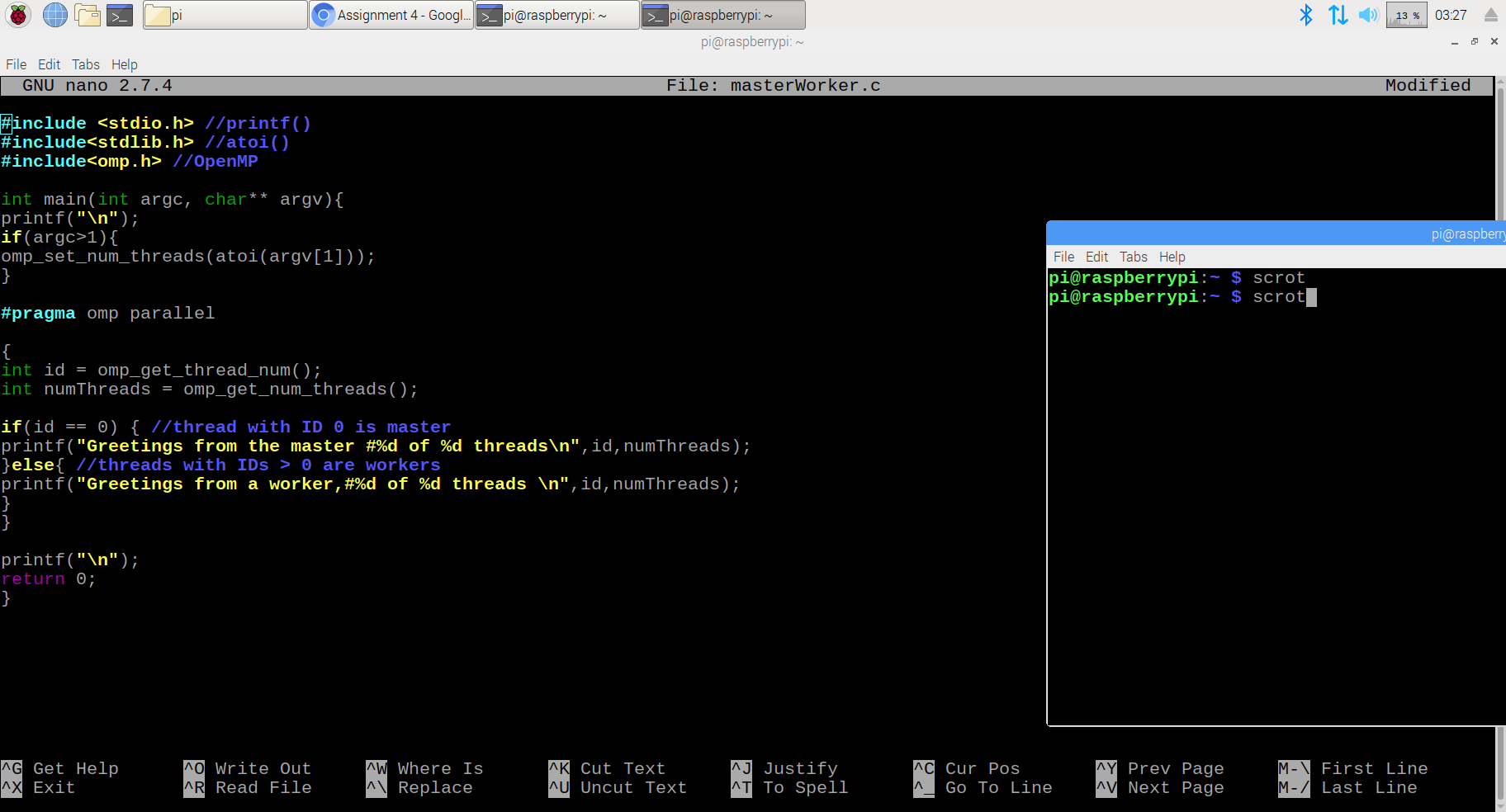
For this portion of the task, I created a file called masterWorker.c and once again copied the code from the provided pdf.



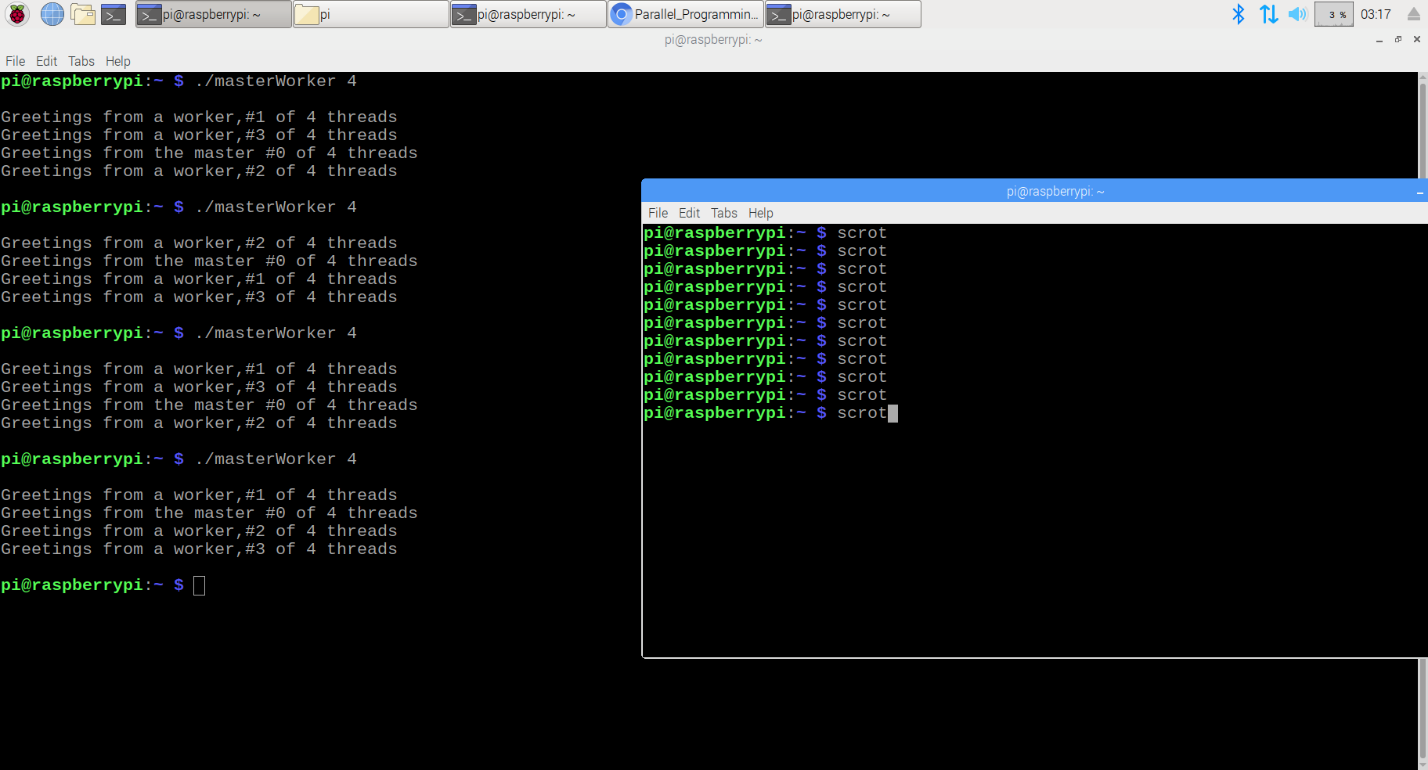
I created an executable to check the results of the program with the “#pragma omp parallel” line commented out.



As expected, only the first condition of the if statement was executed since it was not run parallel. I uncommented the “#pragma omp parallel” line and checked the results again.

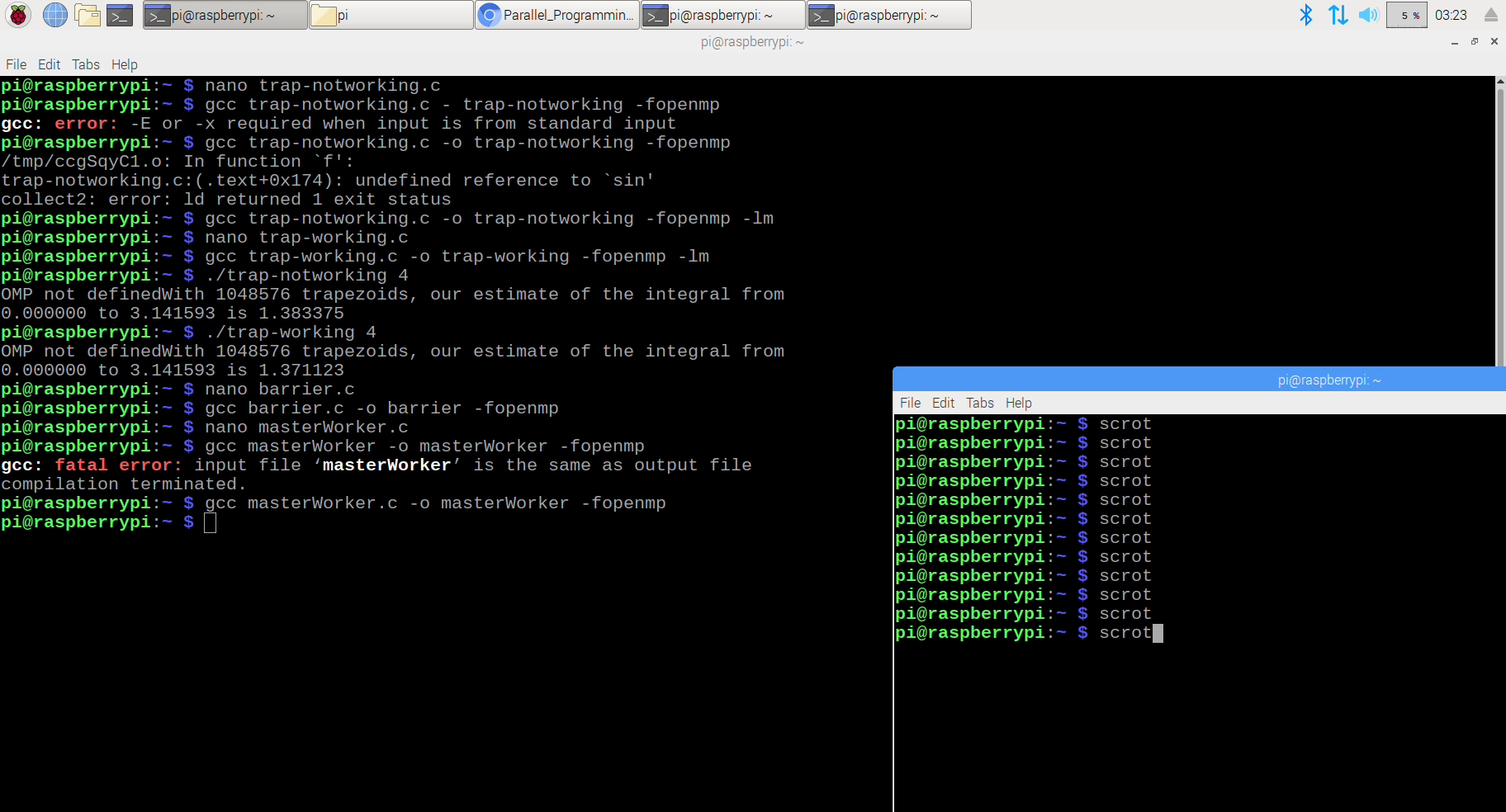


The line is now uncommented. The results shown are below.



Now we are able to see the workers and their corresponding thread number. There are only four threads because it is 4 core. Everything is as expected!

Below is an image of the creation of each of the files and their corresponding gcc line which created the executable for each file.



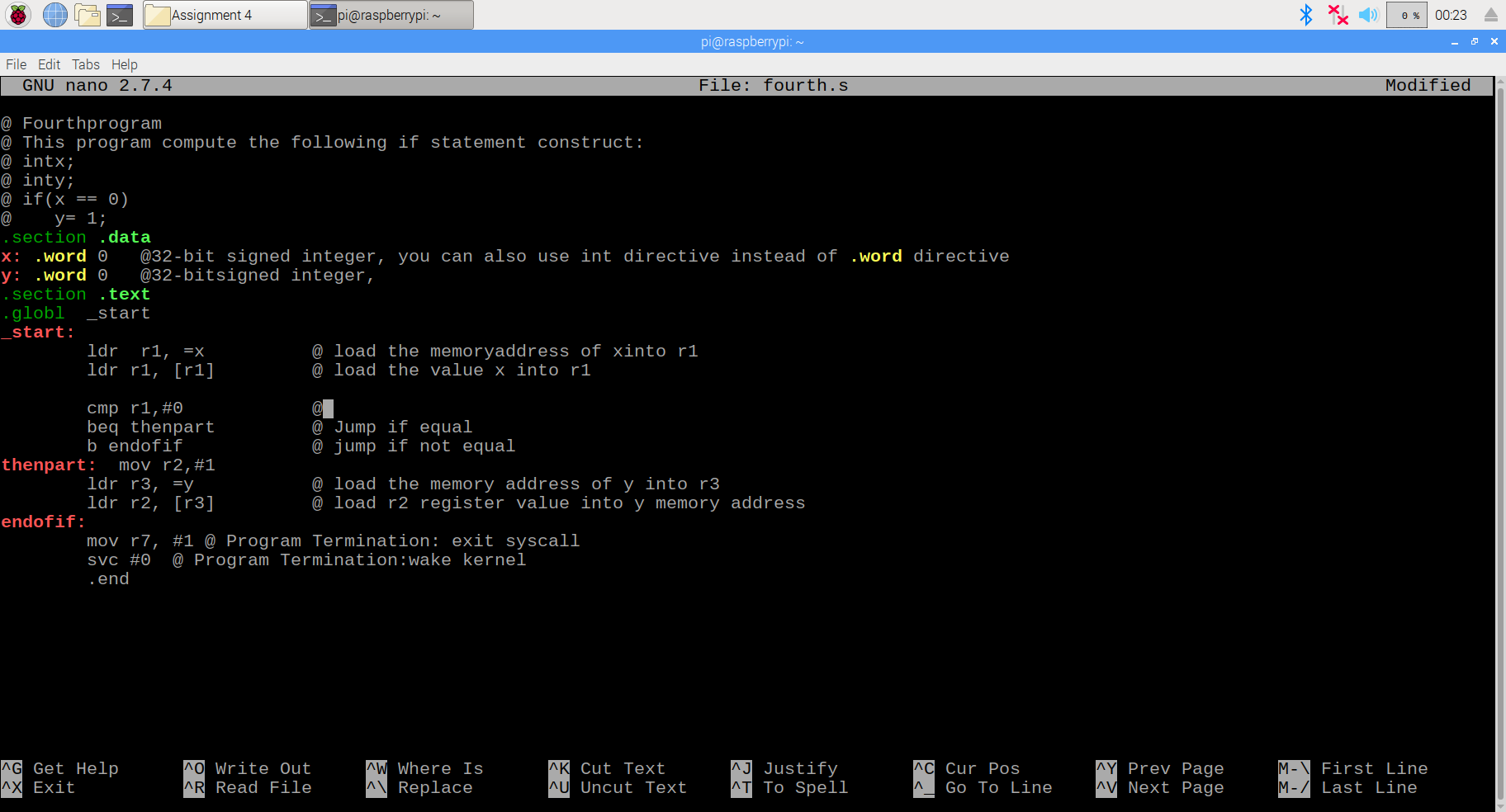
I had to add “-lm” at the end of the gcc lines for the trapezoidal program because it threw an error stating “undefined reference to sin”. That was the only major issue I ran into during the completion of this task.

1. **ARM Assembly Programming**

**3/13** Received the PI

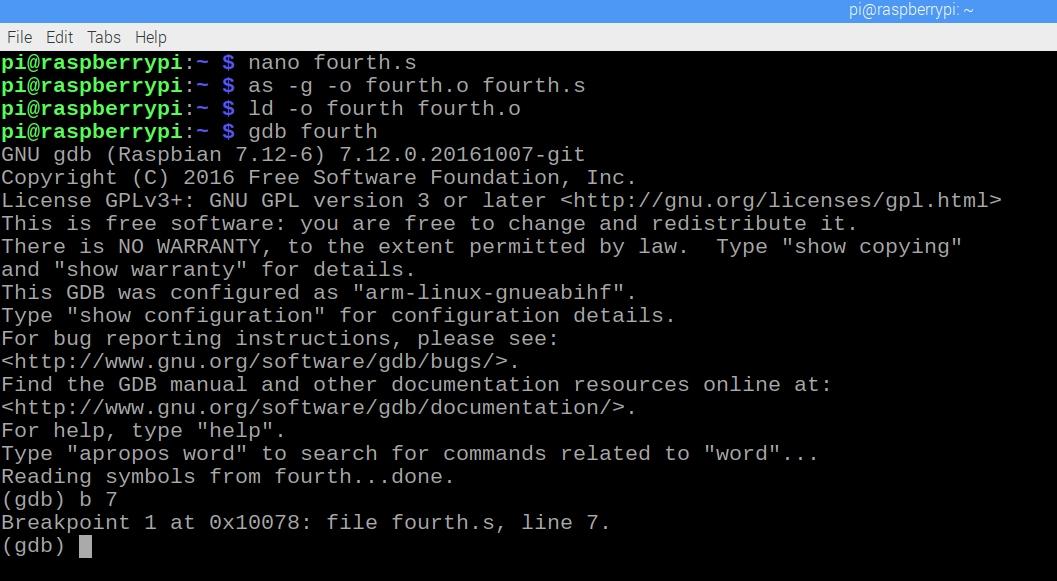
**3/23** Because of the extended time of the break I had extra time till my assignment was due But I finally got around to it. Programming begins.

***Question 1)*** I began by copying the given code into a program file named fourth.s



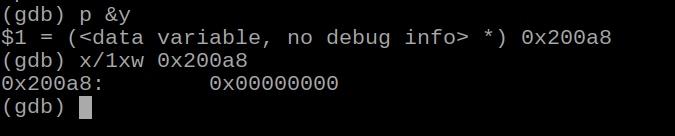
I noticed some issues which would not allow my program to be assembled correctly so I corrected them in the code like adding a “#” before 1 in the line that begins with “thenpart:”

After correcting the mistakes I assembled the program and began the debugging section

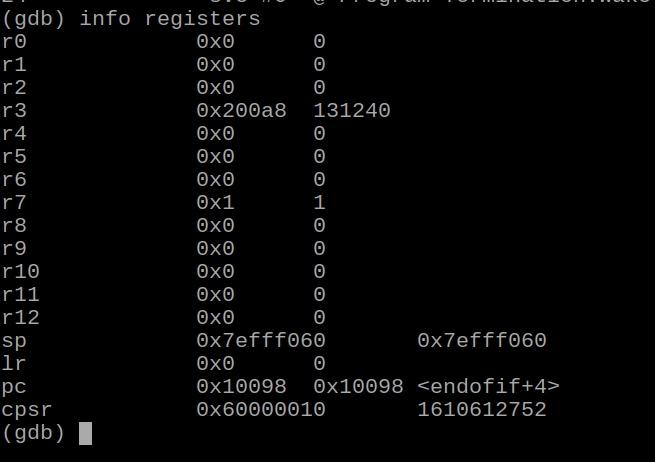


After seeing up the breakpoint and running the program, I stepped through the program far enough to see the affected values.

First the Y (using &y to find the address and then x/1xw address to retrieve the value)



Then I locate the value of the Z-flag by using “info registers” and determining it through the cpsr register

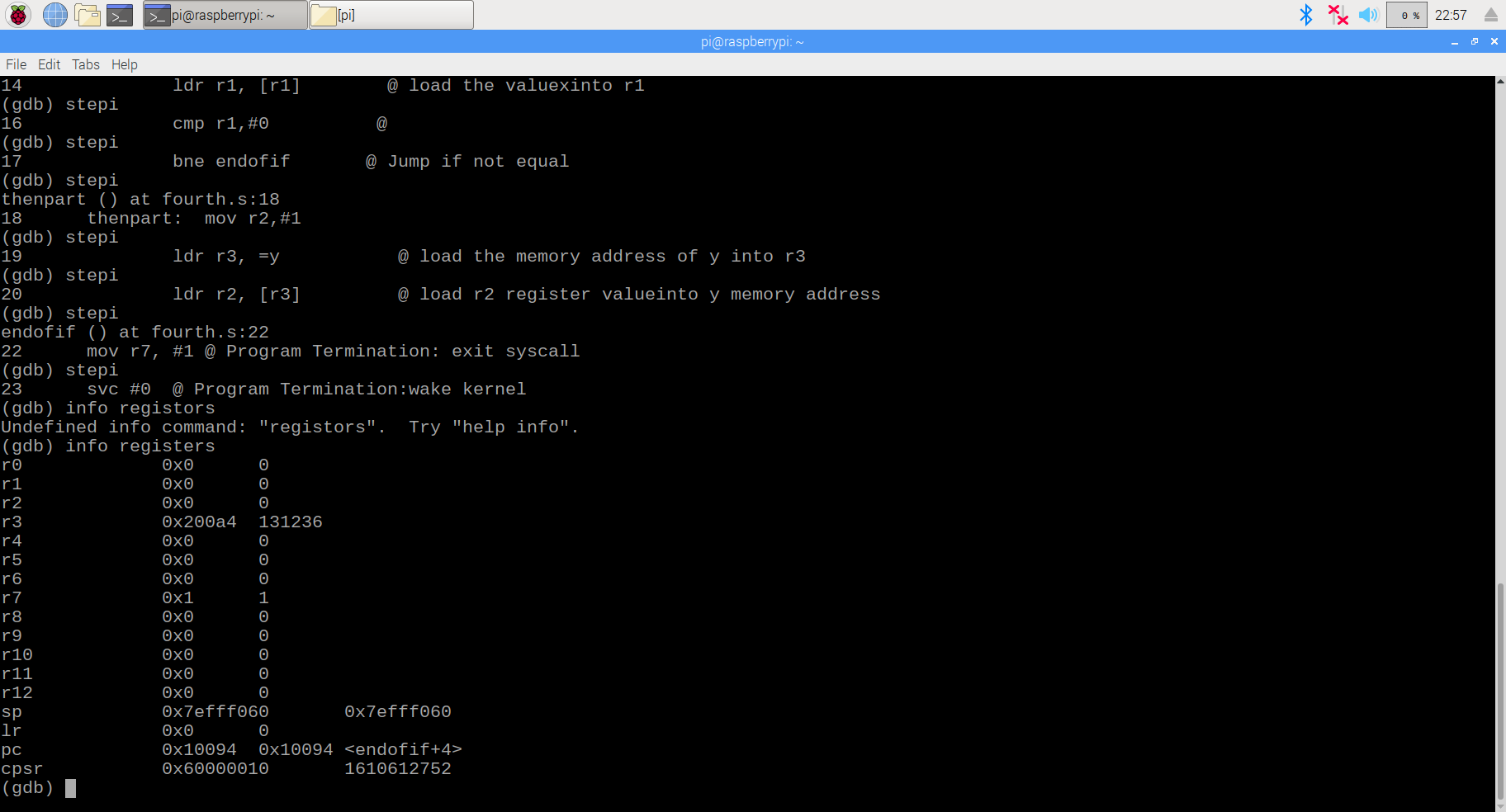


This process was a bit confusing but I learned that cpsr is the register that holds flag values. To find the Z-flag we convert the “60000010” from hex to binary getting 0110 0000 0000… (The rest does not matter for this assignment). We can use prior knowledge to tell that the second binary number from the right represents the status of the Z-Flag. The Z-flag is 1 which is what we are looking for.

***2) Question 2*** - Again I am assuming there was an error in the instructions as it tells us to replace beq with “bnq” however it appears to be referencing “bne” so I used that instead.

Other that that I just removed the the line b endofit and replace the thenpart to endpart.

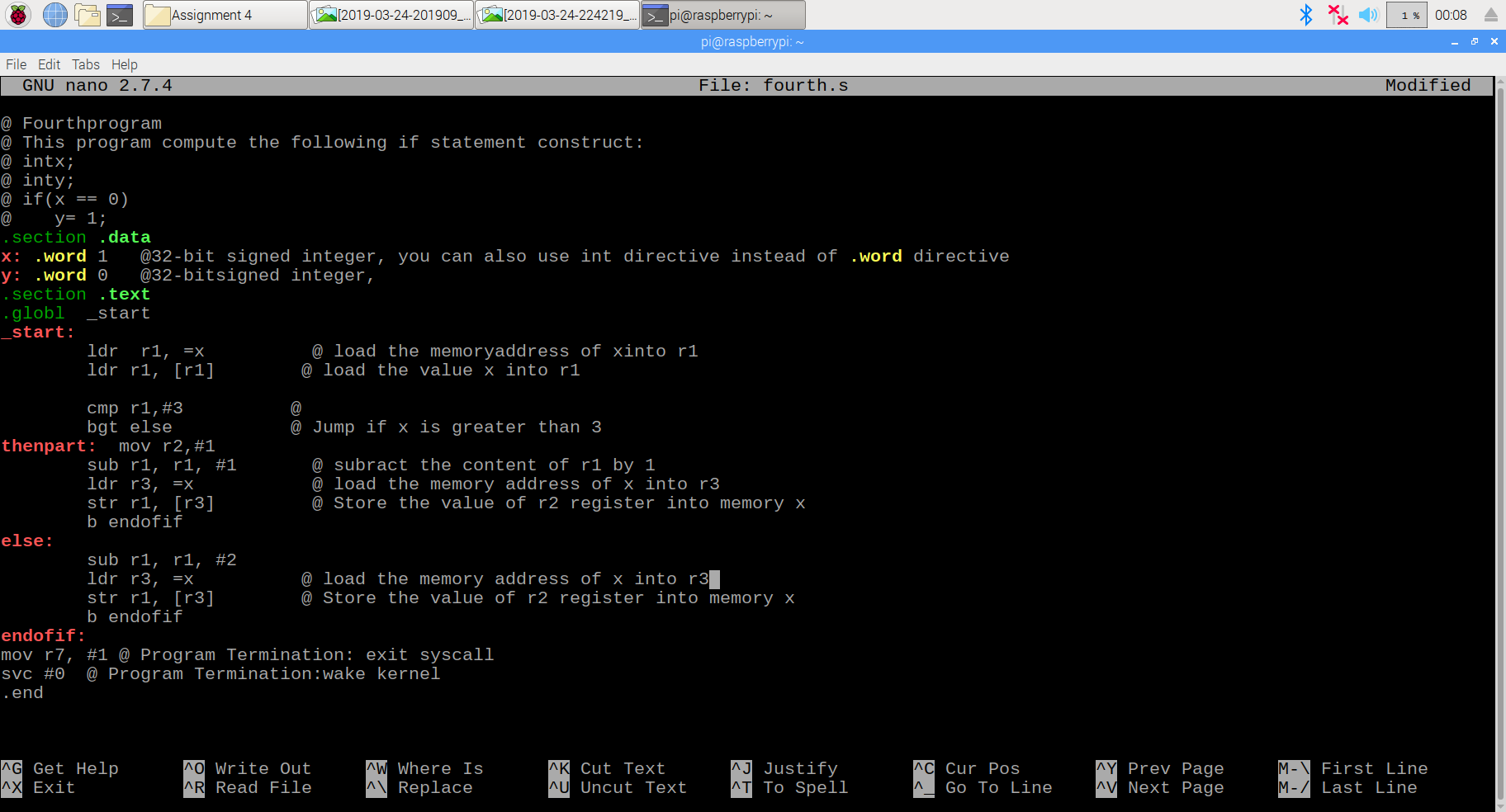
I then assembled the code, ran it through debugging and found the Z-flag through the cpsr just like before.



Just as before, I found cpsr and translated the value given (60...) to binary (01100000…)

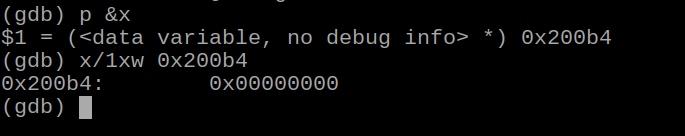
Finding that the Z-flag is set just as expected.

Question 3) - Next I had to edit fourth program to make a specific conditional statement that was specified, the issue here was deciding what conditional branch jump commands that I needed for the particular problem. Here is the code:

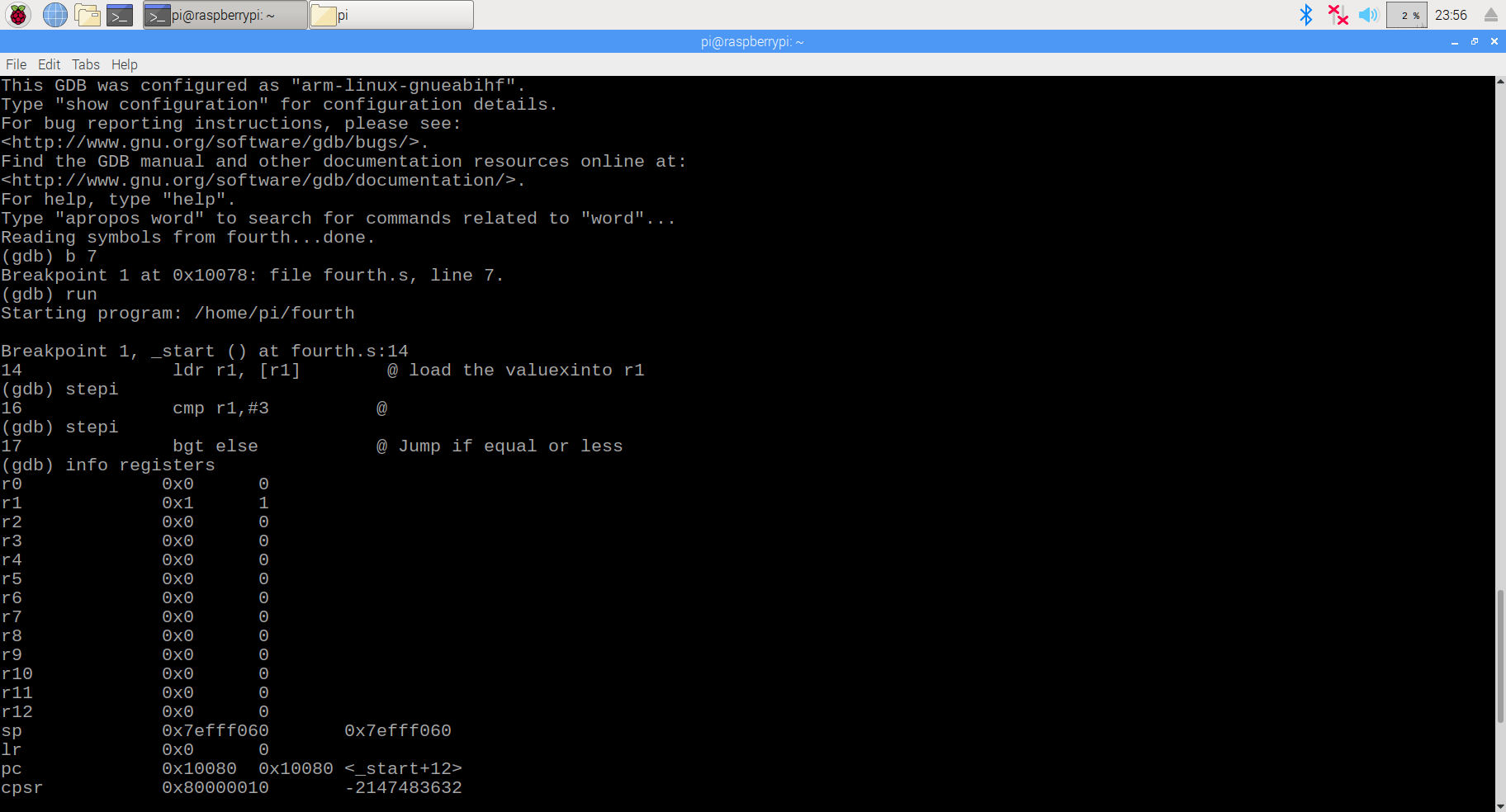


In this code I replace bne with bgt (which jumps to \_\_\_\_ when X is greater than 3) and had it jump to else, thus skipping thenpart. We were asked to find “if x<=3” but it was simpler to use the inverse statement (x>3) to skip to the else statement. I also added a line “b endofif” which skips the else statement and goes straight to the end of the code thus making it impossible to have both if and else happen at the same time.

I found the X value by:



And the Z-flag, the same as before by:



These values matched with what we were looking for using the given x=1. The Z-flag is set to 0 which means that 1-3!=0 which is true. The x value is 0 as 1-3=-2 which is Less than 3 so we would subtract x-1 which is 1-1=0 which is what we got!

1. **Appendix**
2. **Link to Slack, GitHub, YouTube Channel, YouTube Video**

Slack:

<https://csc-3210.slack.com>

GitHub:

<https://github.com/jupitorink>

GitHub Assignment One Repository:

<https://github.com/jupitorink/Assignment4>

YouTube Channel:

<https://www.youtube.com/channel/UChTgBqHzG9G-Mvv-q7uK96g>

YouTube Video:

<https://www.youtube.com/watch?v=ZP6kCrWliCQ>

1. **Task One Screenshot**

Task Assignment Table:

<https://github.com/jupitorink/Assignment-3/blob/master/Task%20Table.png>

1. **Task Two Screenshot**

GitHub Projects Page:

<https://github.com/jupitorink/Assignment4/projects/1>

1. **Task Three Parallel Programming Report, Screenshots**

Parallel Programming Report

<https://github.com/jupitorink/Assignment4/blob/master/parallelprogramming%20report%20assign4.docx>

**E. Task Four ARM Assembly Programming Report, Screenshots**

ARM Assembly Programming Report

<https://github.com/jupitorink/Assignment4/blob/master/Task%204-Write%20up.docx>